

RAPID COMMUNICATION

# Self-formed conductive nanofilaments in (Bi, Mn)O<sub>x</sub> for ultralow-power memory devices



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Received 10 February 2015; received in revised form 24 February 2015; accepted 24 February 2015

Available online 5 March 2015

## KEYWORDS

Nanofilament;  
Operating energy;  
Ultralow power;  
Memory;  
Complex metal oxide

## Abstract

Resistive random access memory (RRAM) is one of the most promising candidates as a next generation nonvolatile memory (NVM), owing to its superior scalability, low power consumption and high speed. From the materials science point of view, to explore optimal RRAM materials is still essential for practical application. In this work, a new material (Bi, Mn)O<sub>x</sub> (BMO) is investigated and several key performance characteristics of Pt/BMO/Pt structured device, including switching performance, retention and endurance, are examined in details. Furthermore, it has been confirmed by high-resolution transmission electron microscopy that the underlying switching mechanism is attributed to formation and disruption of metallic conducting nanofilaments (CNFs). More importantly, the power dissipation for each CNF is as low as 3.8/20 fJ for set/reset process, and a realization of cross-bar structure memory cell is

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demonstrated to prove the downscaling ability of proposed RRAM. These distinctive properties have important implications for understanding switching mechanisms and implementing ultralow power-dissipation RRAM based on BMO.

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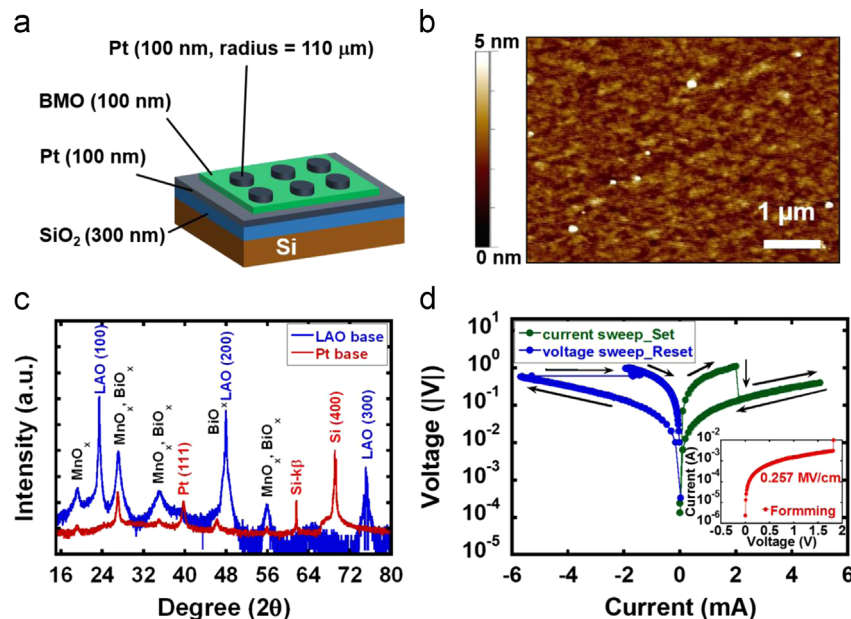
## Introduction

The conventional charge-based memory technologies, such as dynamic random-access memory, static random-access memory and flash memory, are approaching their scaling limits due to the exponentially increased leakage current (charge loss) and the shrunk storage capability of electrons as the memory cell is scaled down [1-3]. Therefore, embedded 3D structure has been explored to provide a new knob on tackling the scaling limit [4-6]. Meanwhile, the non-charged based resistance random access memory (RRAM) with the capability of high cell density in both 2D and 3D-stacked CMOS/RRAM hybrid structure [7,8], have gained increasing attention in both science and technology communities for the past ten years [9-12].

On the other hand, different oxide materials, such as binary metal oxides, have been extensively investigated for RRAM optimization [13,14]. Although it is widely accepted that, regardless of oxide materials, the resistive switching behavior of RRAM is due to the formation and rupture of conducting nanofilaments (CNFs) in oxide, it is found that the physical origins of CNF formation in different oxide materials may not be the same. Several mechanisms have been observed and put forward to explain the causes of the CNF formation in different materials [15-22]. For example, a popularly suggested mechanism is the

crystalline phase change of metal oxides, such as that a conductive ordered  $Ti_4O_7$  phase can be transformed from single crystal  $TiO_2$  in the Pt/ $TiO_2$ /Pt structure *via* joule heating [19]. Another explanation attributes to the oxygen diffusion induced ionic and electron conductivity change of the entire insulating layer, such as in an Au/ $CeO_2$ /Nb-STO structure [20,21]. Moreover, the mobile metal ions from one electrode can travel through solid-state electrolyte to form CNFs under the influence of electric field, such as in Pt-Ir/Cu-GeTe/Cu structure [22]. However, the details of the physical mechanisms of resistive switching are not fully understood yet and still the subject of ongoing research. Therefore a continuing exploration of new RRAM materials and corresponding resistance-switching mechanisms is still of great fundamental importance and will benefit the future development of this area. On the other hand, a superior low power performance can benefit from the downscale capability of RRAM and thus the energy consumption for switching operation of a single CNF could be an ultimate judgment of different RRAM devices [23-27]. However, no detailed studies have been conducted so far to investigate the information of CNF density in RRAM devices due to the difficulty in the observation of CNFs.

Complex metal oxides (CMO), are one of the most important materials for the application in sensors and memories [28-34], based on their unique ferroelectric, ferromagnetic, piezoelectric



**Fig. 1** (a) Schematic of the Pt/BMO/Pt structured device. (b) AFM topographical image of as-deposited BMO films. (c) XRD patterns of BMO films deposited on  $LaAlO_3$  (blue) and Pt/ $Ti/SiO_2/Si$  (red) substrates at 500 °C by pulsed laser deposition. (d) Typical  $I$ - $V$  characteristics of the Pt/BMO/Pt device in current sweep (green dots) and voltage sweep (blue dots) process. The inset shows the  $I$ - $V$  curve of the forming process.

and thermoelectric properties. CMO such as SrTiO<sub>3</sub>, SrZrO<sub>3</sub>, PrCaMnO<sub>3</sub>, LaSrMnO<sub>3</sub> [35-37], have been investigated for memory and logic circuitry applications, due to their stable polarization states. However, the study of CMO based RRAM is still at the early stage, and further research and discussion on various materials and mechanisms are required. In the present study, a CMO material, (Bi, Mn)O<sub>x</sub> (BMO), commonly used as a multi-ferroic material [38], is applied in a Pt/BMO/Pt structured device with demonstration of resistive-switching behavior. The CNF formation in BMO thin film is revealed by high-resolution transmission electron microscopy (HRTEM), showing the CNF composed of pure Bi and Mn atoms due to electrically driven redox mechanism. The RRAM performance is also examined in detail, including forming, set and reset process, size and spacing of CNFs, and switching stability. Furthermore, the ultralow power dissipation in the BMO based devices is discussed. It is revealed that the energy for switching each CNF can be as low as 3.8/20 fJ for set/reset process, even lower than that of single wall carbon nanotube [24]. Finally, to further verify feasibility and downscaling ability of BMO-based RRAM, a cross-bar BMO RRAM array is realized.

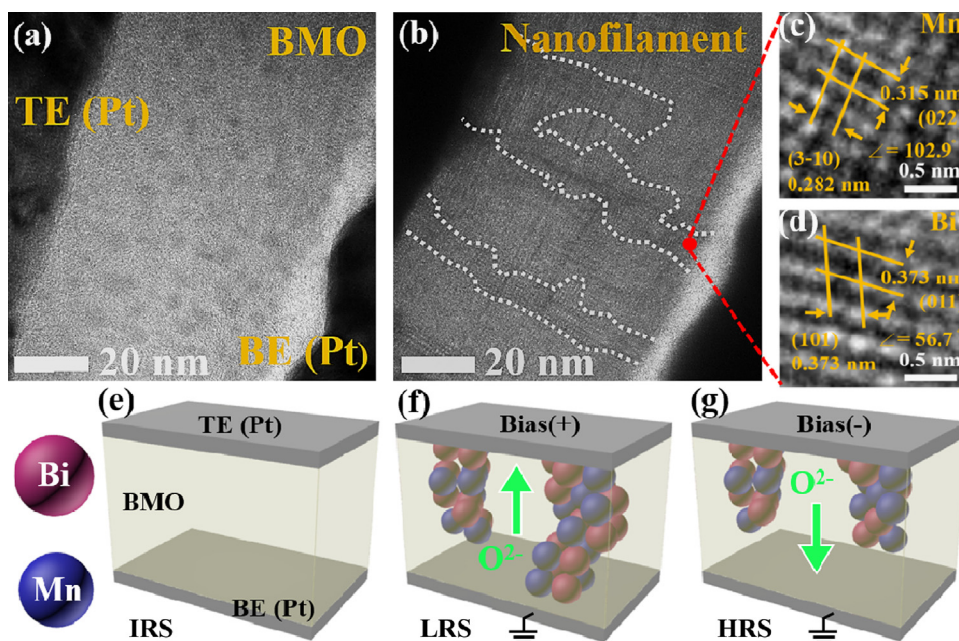
## Experimental section

To fabricate a metal-insulator-metal structured RRAM device on a SiO<sub>2</sub>/Si substrate (Figure 1a), a 5-nm-thick Ti adhesion layer and a 100-nm-thick Pt layer were first deposited by electron-beam evaporation as a bottom electrode (BE), followed by deposition of a 80-nm-thick BMO thin film (Bi<sub>2</sub>O<sub>3</sub> and MnO<sub>2</sub> as precursor materials) by pulsed laser deposition with a KrF laser (COMPex-pro,  $\lambda=248$  nm) at 70 mTorr of oxygen background pressure and temperature at 500 °C. An atomic force microscope (AFM) was then used to characterize the surface morphology of the BMO film (Figure 1b), showing a

surface roughness in nanometer scale. The X-ray diffraction (XRD) patterns of BMO thin films deposited on LaAlO<sub>3</sub> and Pt/Ti/SiO<sub>2</sub>/Si substrates exhibit the identical peaks (Figure 1c), indicating that the main components of the BMO film are the polycrystalline MnO<sub>x</sub> and BiO<sub>x</sub>, which was also verified by the energy dispersive x-ray spectroscopy (EDX). Finally circular top electrodes (TEs) of 110  $\mu\text{m}$  in radius (100-nm Pt) are patterned on top of the BMO film using radio frequency magnetron sputtering, assisted by a metal shadow mask. The electrical measurement of Pt/BMO/Pt devices was carried out with a Keithley 4200 semiconductor parameter analyzer at room temperature. In this work, the forward bias was defined as the current flowing from TE to BE, while the opposite direction is defined as the reverse bias.

## Results and discussion

There are two operation modes for resistive switching of RRAM: bipolar operation mode (BOM) and unipolar operation mode (UOM) [39]. For UOM, the voltage polarities for set and disrupting processes are the same. The cell density in UOM is high due to the fact that RRAM can be stacked on diode selectors, leading to a small footprint  $\sim 4F^2$ , where F is the minimum feature size. However UOM requires a high current induced joule heating during the disrupting process (the reset operation), and thus compromise the stability of devices. On the other hand, CNFs are disrupted with reverse electric field in BOM, usually leading to an improved reliability and a lower power consumption [40]. One suggested mechanism is that oxygen ions in the oxide layer migrate along the direction of electric field, thus assisting the reset process instead of only joule heating [19,41]. The measured *I-V* characteristics for BOM is shown in Figure 1d. Before the switching operation can be accessed, a soft breakdown for the formation of CF in the metal



**Fig. 2** (a-b) Cross-sectional HRTEM image of Pt/BMO/Pt device before and after forming process, respectively. Notice that in (b) two intact nanofilaments connect TE and BE, and the disrupted part of the upper nanofilament is close to the BE. (c-d) HRTEM images of the Mn and Bi crystal information in the nanofilament. (e) Schematic illustration of filament structures of IRS, (f) LRS, and (g) HRS.

oxide, also known as forming process, has to be done first. As shown in the inset of Figure 1d, the current jump at 1.8 V indicates that the electrical stress induced CNF connects the two electrodes (i.e. the breakdown of oxide). After the formation of CF, the Pt/BMO/Pt device is in low resistance state (LRS), considered as a nonvolatile ON state. It is worth noting that the required electric field for establishing breakdown paths is estimated at 0.257 MV/cm for Pt/BMO/Pt device which is much lower than that for other materials reported in the previous literatures [41–43]. After the forming process, the switching operation of RRAM can now be performed. In the reset process (blue curves in Figure 1d), sweeping the voltage from 0 to  $-1$  V results in a sudden current drop at  $-0.6$  V ( $V_{\text{reset}}$ ), indicating the switching from LRS to high resistance state (HRS, the nonvolatile OFF state of the Pt/BMO/Pt device). While in the set process, sweeping current from 0 to 5 mA (green curves in Figure 1d), a sharp voltage drop at 2 mA ( $I_{\text{set}}$ ) occurred, indicating the switching of resistance state back to LRS. Here  $V_{\text{reset}}$  (around  $-0.6$  V) is lower than  $V_{\text{set}}$  (around 1 V), while set and reset currents ( $I_{\text{set}}$  and  $I_{\text{reset}}$ ) in BOM are only 2 and 6 mA, respectively. Therefore current-source sweep in the set process can avoid high compliance current, which dramatically increases the stability of resistive switching.

Figure 2a shows a cross-sectional bright-field TEM image of a fresh Pt/BMO/Pt device. No CNF can be found in the BMO layer before the forming process. Surprisingly, after the device was switched from HRS to LRS, a few CNFs with a diameter of about 10 nm, can be clearly observed between TEs and BEs, as shown in Figure 2b. To identify the material components of CNFs, the Pt/BMO/Pt device is further characterized by HRTEM for more details. Figure 2c and d clearly show d-spacing of 0.315 and 0.373 nm measured in the CF regions, corresponding to the Mn (022) and Bi (011) plane, respectively. Therefore it unambiguously verifies that the CNFs in the BMO layer are composed of Mn and Bi atoms. Hence, the deoxidization of BMO during the forming process could be responsible for the formation of CNFs in Pt/BMO/Pt device.

A schematic mechanism for CNFs formation were shown in Figure 2e–g. Figure 2e is a schematic initial resistance state (IRS) without any CNF. As the electric field is applied, the deoxidization of BMO starts from anode side and generates CNFs. The formed CNF then shortens the gap between electrodes and hence increases current density, resulting in optimal deoxidization conditions in the region between the CF tip and cathode [44]. Therefore, the CNF extends towards cathode along the electric field with time, while the oxygen ions in the BMO migrate in the opposite direction and accumulate near the anode/BMO interface. Once the connection is set, the electrons are able to unobstructedly travel through the BMO layer assisted by Mn and Bi metal atoms crystallized in the CNF region, leading to LRS. Furthermore, a disrupted part of the CNFs is observed close to BE, as shown in Figure 2b, which can be attributed to oxidation of CNFs during the reset process. As the reverse electric field is applied, oxygen ions can migrate to the vicinity of BE/BMO interface, meanwhile the injection current produces heat along CNFs. Thus, sufficient oxygen ions and moderate operation condition together result in the oxidation of CNFs after reset process, leading to HRS. Figure 2e–g schematically illustrate the aforementioned mechanism: the electrical

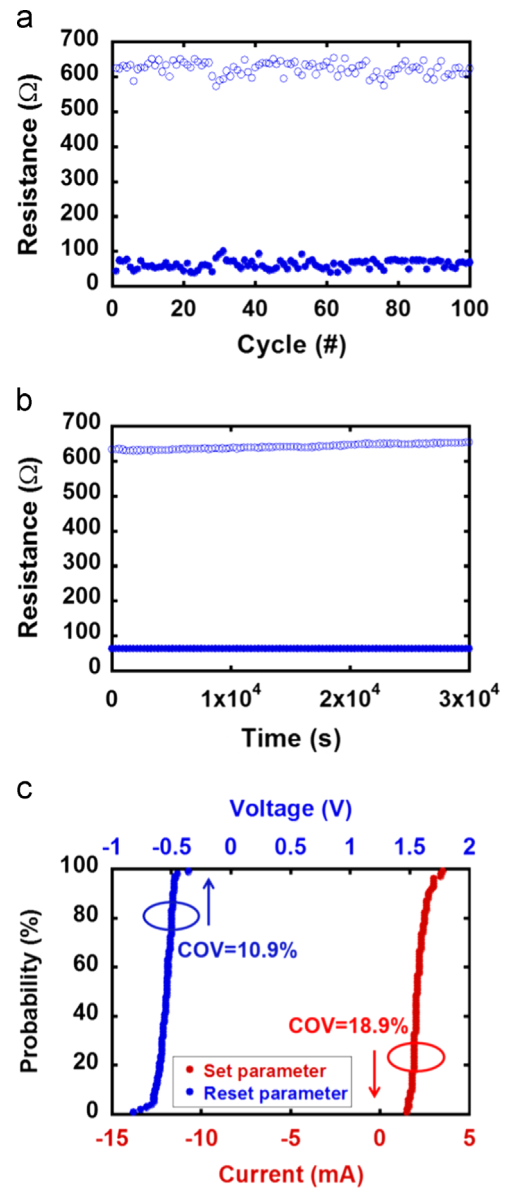


Fig. 3 (a) Resistance evolution of LRS and HRS of a Pt/BMO/Pt device within 100 switching cycles. (b) Data retention characteristic for HRS and LRS; the resistance values in HRS and LRS were read at 100 mV. (c) The distributions of  $I_{\text{set}}$  and  $V_{\text{reset}}$  of a typical Pt/BMO/P device within 100 cycles, and the COV are 18.9% and 10.9%, respectively.

field induced deoxidization of BMO layer generates CNF structures which cause LRS (Figure 2f), and a reversed voltage sweep drives oxygen ions near BE back to the oxide bulk and re-oxidized Mn and Bi, thus switch the cell from LRS to HRS, as shown in Figure 2g.

To further evaluate the reliability and stability of the RRAM device, data retention and endurance characteristics were investigated. Figure 3a illustrates the evolution of LRS and HRS within 100 switching cycles. The resistance values of LRS and HRS were read out at constant voltage of 100 mV for both set or reset process. It is clear that the resistance exhibits a random distribution with a small dispersion, with

LRS ranging from 50 to 100  $\Omega$  and HRS ranging from 580 to 650  $\Omega$ , as the coefficients of variation (COV) of LRS and HRS are 20.2% and 2.98% respectively. Moreover, a superior retention capability of BMO RRAM device is demonstrated in Figure 3b, and no significant change was observed during a time scale of  $3 \times 10^4$  s. The COV of switching parameters  $I_{\text{set}}$  and  $V_{\text{reset}}$  within continuous 100 switching cycles are 10.9%, and 18.9%, respectively (Figure 3c). Compared with conventional device systems such as SiO<sub>2</sub> and HfO<sub>2</sub> based RRAMs (COV of  $V_{\text{set}}/V_{\text{reset}}$  for the SiO<sub>2</sub> and HfO<sub>2</sub> are 13.5%/24.7% and 15%/28.5%, respectively) [7,45], BMO based RRAM exhibits lower operated COV and thus higher stability. Accordingly, the characteristics of low power dissipation and better reliability can be achieved in Pt/BMO/Pt device. Therefore, it validates the potential of BMO based RRAM device for NVM applications.

The density of generated CNFs at a given power is directly related to the power consumption of RRAM devices. Previous studies have shown that the distance between filaments in phase change random-access memory (PCRAM) is in the range of 0.1-5  $\mu\text{m}$  [19]. Assuming that ideally one filament is enough to cause the resistive-switching, the minimum cell size for PCRAM is  $100 \times 100 \text{ nm}^2$ , accordingly. However, to replace the existing memory technologies, further increase of the storage density is still essential. Although various oxide materials have been studied for RRAM application, most of investigations mainly focus on a single memory cell without consideration of the storage density, or in other word, the density of CNFs. In this work, by a statistical summary of HRTEM characterizations (such as Figure 2b and Figure 4a), an average spacing of  $20 \pm 10 \text{ nm}$  between adjacent CNFs can be estimated, indicating that 5 times higher density can be achieved as compared to PCRAM. As a result, the maximum cell density could reach more than 2000 cells/ $\mu\text{m}^2$  in BMO RRAM devices. Another important point to emphasize here is that the average spacing of 20 nm, thus a minimum unit cell of  $20 \times 20 \text{ nm}^2$  (such as

illustrated in Figure 4b), is compatible with the current CMOS technologies [46].

To further estimate the power dissipation in our devices, pulsed switching data (current source, pulse width of 20  $\mu\text{s}$ ) collected from the cells with top electrodes with 100  $\mu\text{m}$  in radius are analyzed to estimate the resistive-switching power consumption per sample area, defined as the switching energy density,  $e_s$ , to be 9.6  $\mu\text{J}/\mu\text{m}^2$  and 50  $\mu\text{J}/\mu\text{m}^2$  for set and reset process, respectively. Apparently lower  $e_s$  results in better power efficiency, and optimization (such as tuning of compliance current) of a particular RRAM device is usually required to achieve a minimum  $e_s$ . However,  $e_s$  is not the only coefficient to determine the power efficiency in different RRAM materials. The density of generated CNFs,  $\alpha_{\text{CF}}$ , is also an important factor, and directly related to the downscaling ability of RRAM device for ultimate power efficiency. Obviously  $\alpha_{\text{CF}}$  varies from one material to another, depending on the intrinsic property of each material. At a given power, a higher  $\alpha_{\text{CF}}$  indicates lower energy to generate and switch each CF. Therefore, the energy required for creating a single CF can be written as  $e_s/\alpha_{\text{CF}}$ , which is now a unified coefficient for characterizing the power efficiency of RRAM material. A point to emphasize here is that  $e_s$  and  $\alpha_{\text{CF}}$  have to be measured in the same device after forming process, to eliminate the error caused by different switching operations. Accordingly, we can estimate  $e_s/\alpha_{\text{CF}}$  in BMO based RRAM to be 3.8 and 20 fJ for set and reset process, respectively. To the best of our knowledge, few related studies have been reported, and the power-dissipation values in this work is among the lowest in previous reports which are in the order of several to hundreds of femto joules. As compared in Table 1, our result is even lower than that of a single wall carbon nanotube [23-27].

Finally to demonstrate the potential in downscaling and low cost manufacture, we fabricated and tested standalone high-density crossbar ( $2 \mu\text{m} \times 2 \mu\text{m}$ ) memory array based on BMO (such as illustrated in Figure 4c). A typical  $I$ - $V$  and switching characteristics of a RRAM cell with the cross-bar

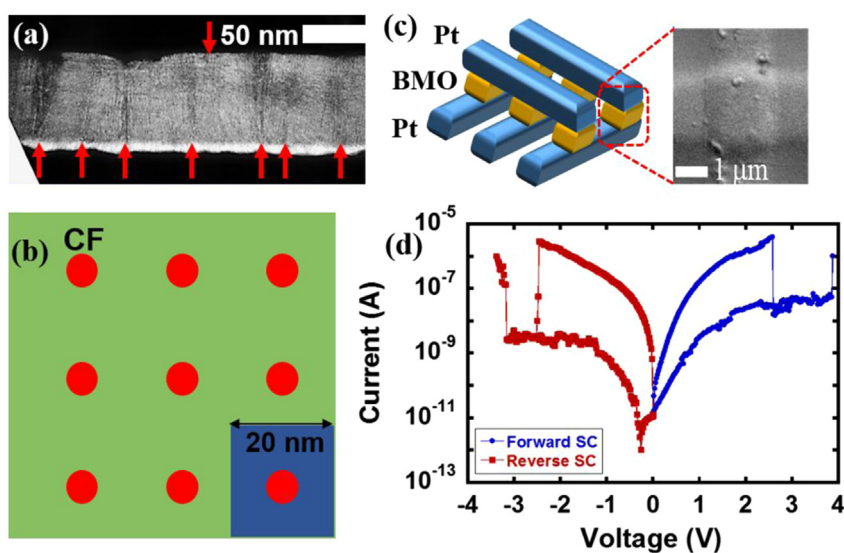


Fig. 4 (a) Cross-sectional TEM image of Pt/BMO/Pt device with 8 CNFs. (b) Schematic illustration of single filament device, and (c) a SEM image of a single unit cell with area about  $2 \times 2 \mu\text{m}^2$  within a cross-bar structure. (d) Typical  $I$ - $V$  characteristics of a cross-bar structured Pt/BMO/Pt device.

**Table 1** Switching energy of different memory structure.

Device Structure	Pt/BMO <sub>x</sub> /Pt	Ni/GeO <sub>x</sub> /HfON/ TaN	Pd/CNT/ Pd	Ni/GeO <sub>x</sub> /HfON/ TaN	GeO <sub>x</sub> /NC-TiO <sub>2</sub> / TaON	Pt/Nb <sub>2</sub> O <sub>5</sub> / Pt
Switching Energy (fJ)	3.8/20 (Set/reset)	6	100	8	720	40
Ref.	This study	[23]	[24]	[25]	[26]	[27]

structure is shown in Figure 4d. To consider the optimal redox conditions in BMO, as reducing the cell size, the operating energy was still fixed. Therefore, the cell needs a higher set or reset voltage for keeping an operating energy. Utilizing the above calculated  $e_S/\alpha_{CF}$  of BMO based RRAM, we can estimate the total power consumed in such device, which is actually in the same order with the result from pulsed switching experiment. Therefore, such crossbar device suggests the feasibility of downscaling and implementation of practical memory structure and logic applications.

## Conclusions

In conclusion, the 80-nm-thick BMO thin film prepared by pulsed laser deposition is first used for nonvolatile memory application. Such devices exhibit excellent resistive switching behaviors and can be operated in BOM with ultralow power consumption (i.e., 3.8 and 20 fJ for set and reset process). The CNF composed of pure metal (Mn and Bi) due to deoxidization is observed for the first time. Also, the morphology of observed CNFs with 10 nm in diameter and an average 20-30 nm spacing suggests the possibility for ultra-high storage density application. Furthermore, excellent reliability, stability and together with a cross-bar RRAM architecture have been demonstrated to further validate the feasibility and downscaling ability of BMO-based RRAM. Thus, the low power dissipation, high cell density, reliable retention, and stable operation of Pt/BMO/Pt structured device suggest that a new member has been introduced to the RRAM family, and thus cast significant influence on future RRAM applications.

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